

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	39	programmable adj interconnect and switching adj matrix	USPAT	OR	OFF	2005/11/21 11:51
L2	3	1 and 326/38.ccls.	USPAT	OR	OFF	2005/11/21 11:51
L3	18	programmable adj interconnect same switching adj matrix	USPAT	OR	OFF	2005/11/21 11:51
L4	16	3 and "326"/\$.ccls.	USPAT	OR	OFF	2005/11/21 11:51
L5	8	3 and "326"/3\$.ccls.	USPAT	OR	OFF	2005/11/21 11:51
S1	4	"006516".ap.	US-PGPUB; USPAT	OR	OFF	2005/11/17 14:36
S2	1	"6480026".pn.	USPAT	OR	OFF	2005/11/17 14:37
S3	1	"6650141".pn.	USPAT	OR	OFF	2005/11/17 14:37
S4	1139	326/38.ccls.	USPAT	OR	OFF	2005/11/17 14:50
S5	384	326/37.ccls.	USPAT	OR	OFF	2005/11/17 14:50
S6	100	S4 and S5	USPAT	OR	OFF	2005/11/17 14:50
S7	37	S6 and 326/41.ccls.	USPAT	OR	OFF	2005/11/17 14:50
S8	10	S7 and 326/47.ccls.	USPAT	OR	OFF	2005/11/17 14:51
S9	2	S8 and 326/101.ccls.	USPAT	OR	OFF	2005/11/17 14:51
S10	863	serializer and deserializer	USPAT	OR	OFF	2005/11/17 14:52
S11	30	S10 and "326"/\$.ccls.	USPAT	OR	OFF	2005/11/17 14:52
S12	10	S11 and S4	USPAT	OR	OFF	2005/11/18 08:17
S13	1139	326/38.ccls.	USPAT	OR	OFF	2005/11/18 08:17
S14	863	serializer and deserializer	USPAT	OR	OFF	2005/11/18 08:17
S15	30	S14 and "326"/\$.ccls.	USPAT	OR	OFF	2005/11/18 08:17
S16	10	S15 and S13	USPAT	OR	OFF	2005/11/18 08:17
S17	1	S16 and input adj output adj buffer	USPAT	OR	ON	2005/11/18 08:20
S18	22	S14 and input adj output adj buffer	USPAT	OR	ON	2005/11/18 08:21
S19	1	S18 and "326"/\$.ccls.	USPAT	OR	ON	2005/11/18 08:21
S20	7	S18 and interconnect	USPAT	OR	ON	2005/11/18 08:22
S22	17	SERDES and "326"/\$.ccls.	USPAT	OR	ON	2005/11/18 08:42
S23	14	S22 and interconnect	USPAT	OR	ON	2005/11/18 08:22
S24	0	S23 and input adj buffers	USPAT	OR	ON	2005/11/18 08:23
S25	0	S23 and input adj buffer	USPAT	OR	ON	2005/11/18 08:23
S26	4	S23 and output adj buffer	USPAT	OR	ON	2005/11/18 08:39
S27	2	S23 and output adj input	USPAT	OR	ON	2005/11/18 08:23
S28	2	S26 and output adj input	USPAT	OR	ON	2005/11/18 08:23
S30	0	S22 and input adj buffer	USPAT	OR	ON	2005/11/18 08:42
S31	11	S22 and input adj output	USPAT	OR	ON	2005/11/18 08:42

S34	8	S22 and input adj output and buffer	USPAT	OR	ON	2005/11/18 08:45
S35	625	(serializer adj deserializer) or SERDES	USPAT	OR	ON	2005/11/18 08:45
S36	12	S35 and programmable adj interconnect	USPAT	OR	ON	2005/11/18 09:11
S37	1	S36 and input adj output adj buffer	USPAT	OR	ON	2005/11/18 08:46
S38	14	FPGA adj programmable adj interconnect	USPAT	OR	ON	2005/11/18 09:11
S39	0	S38 and serializer	USPAT	OR	ON	2005/11/18 09:12
S40	13	programmable adj interconnect and serializer	USPAT	OR	ON	2005/11/18 09:12
S41	9	S40 and buffer	USPAT	OR	ON	2005/11/18 09:12
S42	5	S41 and "326"/\$.ccls.	USPAT	OR	ON	2005/11/18 09:20
S43	5943	input adj output adj buffer	USPAT	OR	ON	2005/11/18 09:20
S44	17	S43 and ((serializer adj deserializer) or SERDES)	USPAT	OR	ON	2005/11/18 09:20
S45	1	S44 and programmable adj interconnect	USPAT	OR	ON	2005/11/18 09:26
S46	7	S44 and interconnect	USPAT	OR	ON	2005/11/18 09:43
S47	625	(serializer adj deserializer) or SERDES	USPAT	OR	ON	2005/11/18 09:43
S48	10	S47 and 326/38.ccls.	USPAT	OR	ON	2005/11/18 09:43
S49	8	S48 and buffer	USPAT	OR	ON	2005/11/18 09:44
S50	5	S49 and interconnect	USPAT	OR	ON	2005/11/18 09:48
S51	1625	(serializer adj deserializer) or SERDES	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/18 09:48
S52	17	S51 and programmable adj interconnect	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/18 09:48
S53	1	S52 and input adj output adj buffer	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/18 09:48
S54	29	S51 and input adj output adj buffer	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/18 09:49
S55	18	S54 and interconnect	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2005/11/18 09:49
S56	0	(serializer adj deserializer) and LVTTTL	USPAT	OR	OFF	2005/11/18 13:32

S57	1	(serializer adj deserializer) and LVCMOS	USPAT	OR	OFF	2005/11/18 13:32
S58	95	(serializer adj deserializer) and PCI	USPAT	OR	OFF	2005/11/18 13:33
S59	3	(serializer adj deserializer) and PCI-X	USPAT	OR	OFF	2005/11/18 13:35
S60	0	LVTTL and LVTTL and LVCMOS and PCI and PCI-X and SSTL and HSTL and GTL+ and CTT and BLVDS and LVDS and LVPECL	USPAT	OR	OFF	2005/11/18 13:36
S61	0	LVTTL and LVTTL and LVCMOS and PCI and PCI-X and SSTL and HSTL and GTL+ and CTT and BLVDS and LVDS and LVPECL	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/18 13:37
S62	0	LVTTL and LVCMOS and PCI and PCI-X and SSTL and HSTL and GTL+ and CTT and BLVDS and LVDS and LVPECL	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/18 13:37
S63	0	LVTTL and LVCMOS and SSTL and HSTL and GTL+ and CTT and BLVDS and LVDS and LVPECL	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/18 13:37
S64	0	LVTTL and LVCMOS and SSTL and HSTL and GTL+ and CTT and LVDS and LVPECL	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/11/18 13:37

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	35	(serializer adj deserializer and programmable).clm.	US-PGPUB; USPAT	OR	OFF	2005/11/21 11:54
L7	4	(serializer adj deserializer and (FPGA or CLPD)).clm.	US-PGPUB; USPAT	OR	OFF	2005/11/21 11:54
L8	168	(serializer adj deserializer).clm.	US-PGPUB; USPAT	OR	OFF	2005/11/21 11:54
L9	16	(serializer adj deserializer and architecture).clm.	US-PGPUB; USPAT	OR	OFF	2005/11/21 11:54
L10	78	(serializer adj deserializer and interface).clm.	US-PGPUB; USPAT	OR	OFF	2005/11/21 11:54